VHF LOW BAND SYNTHESIZED FM TWO-WAY RADIO

TK-620(H) SERVICE MANUAL

KENWOOD

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SUPPLEMENT



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GENERAL/DISASSEMBLY FOR REPAIR

General

FCC COMPLIANCE AND TYPE ACCEPTANCE NUMBERS

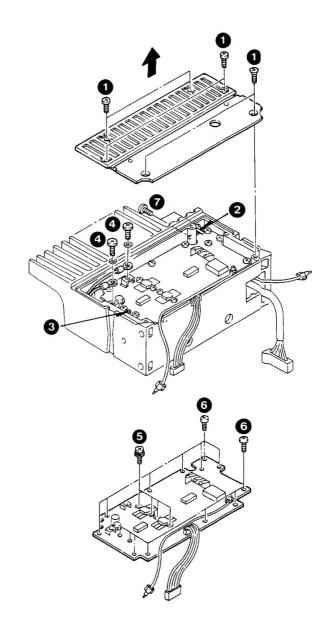
Type acceptance number	Frequency range	Compliance
ALH9TKTK-620H-1	29.7 ~ 37.0MHz	Darta 22 and 00
ALH9TKTK-620H-2	37.0 ~ 50.0MHz	Parts 22 and 90

Disassembly for Repair

Disassembly of Final Unit

1. Final unit (Final) (X45-3240-XX) side

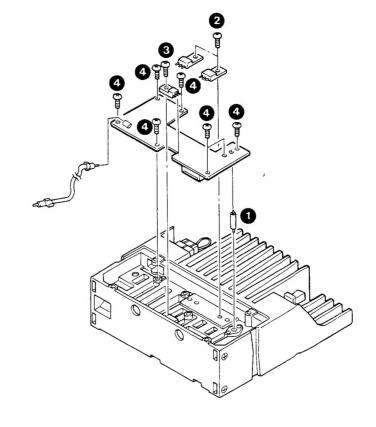
- 1. Remove the six screws () fixing the shielding cover to remove the shielding cover.
- 2. Remove the soldered part (2) of the VHF connector and that (3) of the lead wire (coaxial cable installed through) from the final unit.
- 3. Remove the two screws (4) fixing the lead wires of the power connector.
- 4. Remove the four screws (5) fixing the transistor from the heat sink.
- 5. Remove the 15 screws (**6**) fixing the final unit from the heat sink to remove the final unit.
- 6. Remove the two screws (7) fixing the VHF connector to remove the connector from the heat sink.



DISASSEMBLY FOR REPAIR

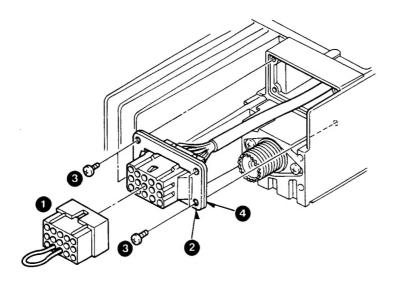
2. Final unit (Drive) (X45-3260-XX) side

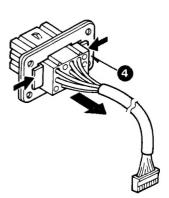
- 1. Remove the soldered part () of the read wire (coaxial cable installed through) from the final unit.
- 2. Remove the two screws (2) fixing the transistor from the hear sink.
- 3. Remove the screw (3) fixing the transistor from the heat sink.
- 4. Remove the seven screws (4) fixing the final unit from the heat sink to remove the final unit.



3. Removal of accessory connector

- 1. Remove the four screws (3) fixing the fitting (2) of the to the heat sink, and pull out the cable and the accessory connector (1) together.
- 2. Remove the packing (♠) from the fitting, then push the part indicated by the arrows (♠) and pull out the connector in the direction of the arrow (♠).





CIRCUIT DESCRIPTION

Transmitter Circuit

The output signal, at the level of approximately 20mW, from Z4 (Transmitter Voltage Controlled Oscillator, TX VCO) is fed through an 8dB attenuator, then it is amplified by RF amplifiers Q101 (2SC2538-22-A), Q102 (TX predriver, 2SC1971) and Q103 (TX driver, 2SC1972), up to a sufficient level for driving final power amplifier transistors.

The driver output signal is applied to each of the final power amplifier transistors Q1 and Q2 (2SC2904 x 2), after going through a two-way power splitter. The transmit signal is amplified to a final transmit power at this stage. The signal is then combined and impedance and matched at the matching circuit. After the above process, the signal passes through K1 (transmit/receive switching relay), a Chebyshev type low-pass filter (L18, L19 and L20), and a forward and reflective power detector circuit, then it is routed to the antenna terminal.

The forward and reflected power detector circuit is located between the low-pass filter and the antenna terminal to maintain output power and to detect antenna load variations to protect the TX power amplifier stages.

The power detector circuit incorporates a broad band dual directional coupler to cover a wide range of frequencies.

The forward power is detected by D2 (1SS101) and is applied to a differential amplifier Q4 and Q5 (2SC3326(A) x 2), where the signal is compared with a reference voltage, after going through VR1 (APC adjustment pot). The output signal from the differential amplifier is applied to Q104 (2SB951A(Q)), which controls a supply voltage to Q101 (RF amplifier), Q102 (TX pre-driver) and Q103 (TX driver) to maintain a constant transmitter output power.

The reflected power is detected by D1 (1SS101), and it is also applied to the differential amplifier (Q4 and Q5).

If the antenna impedance matching becomes abnormal, the output voltage of the reflected power detector increases and this makes the supply voltage, to the power amplifier stages, to drop, causing the transmitter output power to be reduced. This prevents from the output stage transistors being damaged due to the excessive current.

In addition, the temperature rise in the transmitter power amplifier circuit is detected by TH1 (thermister). If the temperature exceeds the preset value because of continuous transmission or an ambient temperature rise, the DC amplifier Q3 (2SC2712(Y)) lowers the reference voltage of the differential amplifier, reducing the transmitter output power to a safe level to prevent damages to the internal components.

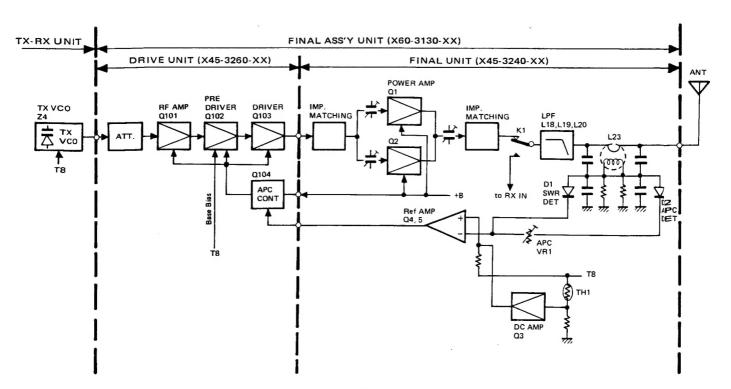


Fig. 1 Transmitter block diagram

CIRCUIT DESCRIPTION

Receiver Circuit

The incoming received signal from the antenna terminal passes through the low-pass filter and the transmit/receive switching relay (K1), then is fed into CN1 in the TX-RX unit.

The signal at CN1 is fed through L13 and L14 (bandpass filter), where undesired signal is eliminated, amplified by RF amplifiers Q6 and Q7 ($2SK582 \times 2$), band limited by another band-pass filter, L17 and L18 then it is applied to D8 (double balanced madulator, ND487C2-3R).

A signal generated by the Common Frequency Synthesizer (COM PLL) is applied as a first local signal to D8, for a down conversion of the receive signal to the first IF (10.7 MHz). It is amplified by Q8 and Q9 (first IF amplifier, 2SK582 x 2).

An incoming signal containing an impulse noise from the antenna is frequency selected at 22MHz and it is amplified by Q1 (2SC2715(Y)) and Q2 (2SC2714(Y)) in Z9 (noise blanker unit, NB unit). It is then AM detected by D1 (HSM88AS).

The detected noise component is waveform shaped into a pulse, synchronizing with the noise, by a following switching circuit Q4 (2SA1162(Y)). The pulse is then inverted by the switching circuit Q5 (2SC2712(Y)), and applied to the gate circuit in the first IF stage.

The gate circuit consists of D9 and D10 (HSM88AS \times 2), If there is no incoming impulse noise, those diodes are held on by the bias from the switching circuit Q5, and passes the IF signal. If an impulse noise is present, the bias from Q5 is interrupted momentarily, and the gate turns off to prevent the IF signal from passing through.

The DC component detected by D1 is amplified by Q3 (2SC2715(Y)) and used for an automatic gain control of the RF amplifiers Q1 and Q2, in order to prevent a satura-

tion of the RF amplifier stages in a high field intensity area and to secure noise detection sensitivity.

The IF signal is filtered by XF1 (6-pole monolithic crystal filter), and amplified again by Q12 (IF amplifier, 2SK302(GR)), and is sent to Z8 (module unit (IF)).

The first IF signal sent to IC1 (FM IF system, MC3361D) in Z8. The signal is heterodyned again to 455kHz, using the second local signal generated by the crystal oscillator (11.155MHz), at the second mixer in IC1. The second IF signal is filtered by CF1 (ceramic filter), amplified by limiting amplifiers in IC1, and is detected as an FM signal.

The detected signal is sent from Z8 as the DET OUT signal to Z10 (band-pass filter/voltage controlled amplifier), where the signal is amplified. After this amplification, the signal is routed to CN7 as the DET signal. Unless the optional signaling unit is connected, the signal is looped back to CN7 by a short plug (CN15) and it is routed back to Z10 again. In Z10, the signal passes through a low-pass filter and high-pass filter consists of IC2 (NJM4558M) to attenuate any signals below 300Hz or above 3kHz. The signal then enters an electronic volume control (IC3, M5222FP).

The volume control changes a DC voltage applied to IC3, which controls the gain of IC3. The output of IC3 has a -6dB/Octave de-emphasis characteristics for a frequency range between 300Hz and 3kHz. This signal passes through Q16 and Q17 (squelch switch, 2SC3326(A) x 2), and it is amplified to drive the speaker by IC1 (audio-frequency power amplifier, μ PC1242H).

In addition to the receive signal, the ALERT signal from the signaling unit is applied to IC1, therefore, an alert tone can be head from the speaker if so an optional accessory board is installed. This IC1 output is sent to the accessory connector, J3, of the final unit, and drives an internal speaker connected to it. And an external speaker may be driven directly by connecting it to J3.

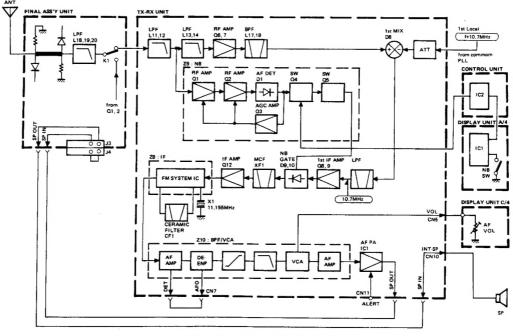


Fig. 2 Receiver block diagram

SEMICONDUCTOR DATA/DESCRIPTION OF COMPONENTS

Semiconductor Data

2SC2904: Power amplifier (Final unit Q1, 2)

• Electrical characteristics

Item	Symbol	nbol Condition		Rating			
110111	V(BR)CBO Ic = 20mA, I _E = 0 V(BR)EBO I _E = 20mA, I _C = 0	MIN.	TYP.	MAX.	Unit		
Collector - base breakdown voltage	V _(BR) CBO	Ic = 20mA, I _E = 0	50			٧	
Emitter · base breakdown voltage	V(BR)EBO	I _E = 20mA, Ic = 0	5			V	
Collector - emitter breakdown voltage	V(BR)CEO	Ic = 0.1A, R _{BE} = ∞	20			٧	
Collector cut-off current	Ісво	V _{CB} = 15V, I _E = 0			5	mΑ	
Emitter cut-off current	1 _{EBO}	V _{EB} = 3V, Ic = 0			5	mΑ	
DC current amplification factor	hFE	V _{CE} = 10V, Ic = 1A	10	50	180	_	
Output power	PO	f = 30MHz, Vcc = 12.5V, Pin = 7W	100	110		W	
Collector efficiency	η_{C}	f = 30MHz, Vcc = 12.5V, Pin = 7W	55	60		%	

(Tc = 25 °C)

Description of Components

FINAL ASS'Y UNIT (X60-3130-XX)

FINAL UNIT (X45-3240-XX): FINAL ASS'Y

Component	Parts No.	Use/Function	Operation/Condition/Compatibility
Q1, 2	2SC2904	Power amplifier	
Ω3	2SC2712(Y)	Protector amplifier	Temperature.
Q4, 5	2SC2712(Y)	APC controller	
D1,2	1SS101	Detector	
D3	1SS184	Separator	
D4	SG-5LR	Protection	Contrary polar protection.
D5	1SS184	A standard voltage	
D6	1SS184	Protector	

DRIVE UNIT (X45-3260-XX): FINAL ASS'Y

Component	Parts No.	Use/Function	Operation/Condition/Compatibility
Q1O1	2SC2538-22-A	RF amplifier	
Q102	2SC1971	Pre-driver amplifier	
Q103	2SC1972	Driver amplifier	
Q104	2SB951A(Q)	APC amplifier	
D1O1, 102	1SS184	Bias control	

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* New Parts
Parts without Parts No. are not supplied.
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Ref. No.	Address	New Parts	Parts No.	Description	Desti- nation	Re- marks
			Т	K-620 (H)	,	<u></u>
1 2 4	3B 1B 1A		A01-1031-12 A01-1064-02 A20-2616-13	CASE (LOWER) CASE (UPPER) PANEL ASSY		
6 7 7 8 10	1A 1A 1A 1A 3B	*	B03-0545-03 B03-0552-03 B03-0553-03 B11-0437-04 B40-3954-04	DRESSING PLATE (TX-BUSY-CALL) DRESSING PLATE (MONI-NB) DRESSING PLATE (SCAN) FILTER MODEL NAME PLATE	K	
10 11 12	3B 2D 2D	*	B40-3955-04 B46-0409-20 B50-8302-00	MODEL NAME PLATE WARRANTY CARD INSTRUCTION MANUAL	K2	
14 15 CN15 J4 W8	1D 1C 2B 1A	*	E30-2036-05 E30-2158-05 E31-3270-05 E31-3228-05 E31-3225-05	GROUND LEAD DC CORD SHORT PLUG (12P) SHORT PLUG (ACC) CONNECTING WIRE (SP)		
20 21 22	1D 3A 2A		F06-2524-05 F11-1057-03 F11-1121-03	FUSE (25A) SHIELDING COVER (LOWER) SHIELDING COVER (UPPER)		
27 28 29 30	2A 1A 1A 3B		G09-0405-05 G09-0418-05 G10-0652-04 G10-0663-04	SPRING (CH) SPRING (VOL) FELT (SP) FELT (100 x 85)		
31 32 33 34	1B, 3B 1B, 3B 1B, 3B 1A		G10-0667-04 G16-0516-14 G16-0517-04 G53-0535-04	FELT (150 x 10) SHEET (150 x 100) SHEET (20 x 130) PACKING (MONI/NB)		
37 38 39 40 41	2D 3C 2C 2D 1C	*	H02-0605-04 H01-8268-04 H10-2647-03 H10-2654-03 H12-1415-04	INNER PACKING CASE ITEM CARTON BOX POLYSTYRENE FOAMED FIX (FRONT) POLYSTYRENE FOAMED FIX (REAR) PACKING FIXTURE		
41-1 42 43 44 45	3D 1D 1D 2D 1C		H11-0827-04 H25-0123-04 H25-0096-04 H25-0724-04 H25-0726-04	PACKING FIXTURE PROTECTION BAG (DC CORD) PROTECTION BAG (FUSE) PROTECTION BAG (RADIO) PROTECTION BAG (MIC)		
47 48 49 50 51	1D 1A 1A 1D 1D		J19-1376-15 J21-4214-03 J21-4215-03 J29-0422-13 J54-0060-03	MIC HANGER MOUNTING HARDWARE (VOL/SQL) MOUNTING HARDWARE (SP) MOUNTING BRACKET STAY		
53 54 55 56	1A 2A 1A 1A		K27-0498-04 K29-3075-03 K29-3076-13 K29-3077-03	PUSH KNOB (MONI, NB) KNOB (CH) KNOB (VOL) KNOB (SQL)		
59 A B C	1D 1A, 1B 1A 1A, 2D		N89-2608-41 N99-0330-05 N09-0673-05 N09-0675-05 N09-2030-05	BINDING HEAD TAPTITE SCREW SCREW SET SCREW (M3 x 6, SP) SCREW FLAT HEAD MACHINE SCREW		

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D E	1B, 3B 2A, 3A		N35-3008-45 N87-2606-46	BINDING HEAD BINDING HEAD				
62 63	1A 2C		T07-0247-05 T91-0362-05	LOUDSPEAKER MICROPHONE	(FULL RA			
67 68 69 69 70	2A 1A 3A 3A 2B	*	X53-3060-10 X54-3040-10 X57-3260-10 X57-3260-11 X60-3130-10	CONTROL UNIT DISPLAY UNIT TX-RX UNIT TX-RX UNIT FINAL ASSY			K K2 K	
70	2B	*	X60-3130-11	FINAL ASSY			K2	
		FII	NAL UNIT (X45-3	240-XX) -10:	K -11 : F	(2		
C1 C2 C2 C3 C8, 9			CM73F2H331J CM73F2H560J CM73F2H750J CC73FCH1H471J CM73F2H751J	CHIP C CHIP C CHIP C CHIP C CHIP C	330PF 56PF 75PF 470PF 750PF)))	K2 K K	
C8, 9 C10, 11 C12, 13 C14, 15 C16, 17			CM73F2H821J C90-2025-05 C91-1056-05 CE04EW1E471M CM73F2H471J	CHIP C ELECTRO CHIP C ELECTRO CHIP C	820PF 4.7UF 4700PF 470UF 470PF	J 25WV K 25WV J	K2	
C16, 17 C18 C18 C22 C22			CM73F2H681J CM73F2H151J CM73F2H221J CM73F2H470J CM73F2H750J	CHIP C CHIP C CHIP C CHIP C CHIP C	680PF 150PF 220PF 47PF 75PF	J J	K K K2 K2 K	444
C23 C24 C25 C25 C25 C26			CM73F2H471J C91-1056-05 CM73F2H620J CM73F2H820J CM73F2H080D	CHIP C CHIP C CHIP C CHIP C CHIP C	470PF 4700PF 62PF 82PF 8.0PF	J K J J D	K2 K K	
C27 C27 C28 C28 C29			CM73F2H101J CM73F2H151J CM73F2H180J CM73F2H220J CM73F2H121J	CHIP C CHIP C CHIP C CHIP C CHIP C	100PF 150PF 18PF 22PF 120PF)))	K2 K K2 K	
C29 C30 C30 C31 C31			CM73F2H820J CM73F2H430J CM73F2H510J CM73F2H330J CM73F2H470J	CHIP C CHIP C CHIP C CHIP C CHIP C	82PF 43PF 51PF 33PF 47PF]]]	K2 K2 K K2 K2	
C32, 33 C34 C34 C35 C36 – 47			CM73F2H020C CC73FCH1H560J CC73FCH1H820J CC73FCH1H330J CK73FB1H472K	CHIP C CHIP C CHIP C CHIP C CHIP C	2.0PF 56PF 82PF 33PF 4700PF	C J J K	K2 K	
TC1, 2 TC3			C05-0367-05 C05-0336-05	TRIMMING CAP. TRIMMING CAP.				
A1 CN1 CN2, 3			E29-0468-14 E04-0159-05 E23-0491-05	LEAD PLATE RF COAXIAL CAB TERMINAL	(ANT) LE RECEP	TACLE		

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CN4 W1			E40-5166-05 E31-3273-05	PIN CONNECTOR W		(6P) (5P)		
A4			F10-1391-04	SHIELDING PLA	ΓE			
A2			J31-0531-04	COLLAR				
A3 L1 L1 L2, 3 L4, 5			L34-1190-05 L34-1243-05 L34-1244-05 L34-1148-05 L39-0421-05	COIL COIL COIL COIL	4ø7T 4ø8T 6ø1T		K2 K	
L4, 5 L6, 7 L8, 9 L10, 11 L12, 13			L39-0427-05 L34-0742-05 L34-1134-05 L34-1147-05 L34-1249-05	COIL COIL COIL COIL	3ø5T 5ø5T 4ø7T		K K2	
L12, 13 L14, 15 L14, 15 L16 L16		*	L34-1250-05 L34-1146-05 L34-1257-05 L34-1151-05 L34-1240-05	COIL COIL COIL COIL	6ø1.5T 6ø2.5T 6ø6T 6ø5.5T		K K2 K K	
L17 L18 L18 L19 L19			L33-0692-05 L34-1150-05 L34-1241-05 L34-1151-05 L34-1241-05	CHOKE COIL COIL COIL COIL	27UH 6ø8T 6ø7T 6ø6T 6ø7T		K K2 K2 K	
L20 L20 L21, 22 L23 L24, 25		*	L34-1256-05 L34-1259-05 L40-2201-81 L39-0447-05 L92-0110-05	COIL COIL SMALL FIXED IN TROIDAL COIL CORE	6ø4.5T 6ø5.5T IDUCTOR	22UH	K2 K	
R1 R2 R2 R3 R3			RS14AB3D1R5J RD14FB2B682J RD41FB2B392J RD41FB2B330J RD41FB2B390J	FL-PROOF RS CYLND CHIP R CYLND CHIP R CYLND CHIP R CYLND CHIP R	1.5 6.8K 3.9K 33 39	J 2W J 1/8W J 1/8W J 1/8W J 1/8W	K K2 K2 K	
R4 R5 R6 R7 R8			RD41FB2B560J RD41FB2B153J RD41FB2B100J RD41FB2B470J RD41FB2B103J	CYLND CHIP R	56 15K 10 47 10K	J 1/8W J 1/8W J 1/8W J 1/8W J 1/8W		
R9 R10 R11 R12 R13			RD41FB2B122J RD41FB2B392J RD41FB2B102J RD41FB2B221J RD41FB2B102J	CYLND CHIP R CYLND CHIP R CYLND CHIP R CYLND CHIP R CYLND CHIP R	1.2K 3.9K 1.0K 220 1.0K	J 1/8W J 1/8W J 1/8W J 1/8W J 1/8W		
R14 R15 R16, 17 VR1			RD41FB2B122J RS14AB3A470J RS14AB3B100J R12-3430-05	CYLND CHIP R FL-PROOF RS FL-PROOF RS TRIMMING POT.	1.2K 47 10 10K	J 1/8W J 1W J 1W		
K1			S51-1437-05	RELAY	9V DC			
D1, 2 D3 D4			1SS101 1SS184 SG-5LR	DIODE CHIP DIODE DIODE				

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D5, 6 Q1, 2 Q3 – 5 TH1	2A		1SS184 2SC2904 2SC2712(Y) SDT1000	CHIP DIODE TRANSISTOR CHIP TRANSISTOR THERMISTOR 1	0K		
		DR	RIVE UNIT (X45-3	260-XX) -10 : K -1	1 : K2		
C101 C102 C103, 104 C105 C106			CC73FCH1H680J CC73FCH1H331J CK73FB1H472K CE04EW1C100M CK73FB1H472K	CHIP C 68PF CHIP C 330F CHIP C 4700 ELECTRO 10UI CHIP C 4700	PF J DPF K F 16WV		
C107 C108, 109 C110 C111 C112			CC73FCH1H221J CK73FB1H472K CK73EB1H472K CK73FB1H472K CK73FB1E104K	CHIP C 220F CHIP C 470C CHIP C 470C CHIP C 470C CHIP C 0.10	OPF K OPF K OPF K		
C113 C114 C115, 116 C117 C118			C90-2022-05 CK45B1H152K C91-1056-05 CK73EB1E104K C90-2022-05	OS 22UI CERAMIC 1500 CHIP C 4700 CHIP C 0.10 OS 22UI	OPF K OPF K UF K		
C119 C119 C120 C121, 122 C121, 122			CC73FCH1H221J CC73FCH1H331J CC73FCH1H221J CC73FCH1H221J CC73FCH1H331J	CHIP C 220F CHIP C 330F CHIP C 220F CHIP C 220F CHIP C 330F	PF J PF J	K2 K K2 K	
C123 C124 C125 C126, 127 C128			C91-1056-05 CK73FB1B472K C90-2044-05 CK73FB1H472K CC73FCH1H331J	CHIP C 4700 CHIP C 4700 ELECTRO 1UF CHIP C 4700 CHIP C 330F	DPF K 25WV DPF K		
C129			CC73FCH1H101J	CHIP C 100F	PF J		
CN101 CN102			E04-0159-05 E40-0608-05	RF COAXIAL CABLE RE PIN CONNCTOR (6P)	ECEPTACLE		
L101 L102 L103 L104 L105			L34-0893-05 L40-2292-14 L34-0894-05 L33-0666-05 L34-0908-05	COIL 3ø4T SMALL FIXED INDUCT COIL 3ø5T CHOKE COIL COIL 3ø9.	OR 2.2UH F		
L106 L107 L108 L109 L110		*	L33-0666-05 L34-1113-05 L34-0742-05 L34-1242-05 L34-0742-05	CHOKE COIL COIL 3ø1. COIL 3ø5. COIL 4ø6. COIL 3ø5.	Γ Γ	K	
L110 L111 L111 L112 L113			L34-1039-05 L34-1243-05 L34-1244-05 L33-0666-05 L34-1025-05	COIL 4ø1. COIL 4ø77 COIL 4ø87 CHOKE COIL COIL 3ø5.	r r	K2 K2 K	
L114, 115			L92-0110-05	CORE			
R101 R102 R103			RD41FB2B121J RD41FB2B470J RD41FB2B121J	CYLND CHIP R 4	20 J 1/8W 7 J 1/8W 20 J 1/8W		

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Ref. No.	Ąddress	Address New Parts No. Description								Desti- nation	Re- marks
R104 R105 R106 R106 R107			RD41FB2B222J RD41FB2B103J RD41FB2B121J RD41FB2B560J RD41FB2B180J	CYLND CHIP R	2.2K 10K 120 56 18	J J	1/8W 1/8W 1/8W 1/8W 1/8W	K2 K			
R108 R109 R110 R111			RD41FB2B101J RD14BB2C100J RD14BB2C271J RD14BB2C121J RD14BB2C680J	CYLND CHIP R RD RD RD RD	100 10 270 120 68	J	1/8W 1/6W 1/6W 1/6W 1/6W	K2 K			
R112 R113 R113 R114 R116			RD14BB2C470J RS14AB3A121J RS14AB3A680J RD41FB2B102J RS14DB3H101J	RD FL-PROOF RS FL-PROOF RS CYLND CHIP R FL-PROOF RS	47 120 68 1.0K 100	J	1/6W 1W 1W 1/8W 5W	K2 K			
R116			RS14DB3H470J	FL-PROOF RS	47	J	5W	К			
D101, 102 Q101 Q102 Q103 Q104	3B 3A 3A		1SS184 2SC2538-22-A 2SC1971 2SC1972 2SB951A(Q)	CHIP DIODE TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR							
		FIN	IAL ASS'Y (X60-	3130-XX) -10 : F	(-11:	K2					
J1 J2 J3 W4 W5	2B 2B 3B 3A 2A		E04-0167-05 E31-3383-05 E31-3384-05 E31-3256-05 E31-2104-05	RF COAXIAL CAB CONNECTOR WIT CONNECTOR WIT CONNECTOR WIF CONNECTOR WIT	H LEAD H LEAD RE	TACL (RX					
W6	3B		E31-3123-05	CONNECTOR WIT	H LEAD	(TX	()				
A1 A2	2A 1B		F01-0964-01 F11-1107-02	HEAT SINK SHIELDING COVE	R						
A3	2B		G53-0540-04	PACKING							
A4 A5	2B 2B		J21-4241-04 J21-4242-04	MOUNTING HARD			SUPPLY)				
100 E K L	1A 1B 2A 1A 2B		N16-0040-41 N35-3008-45 N09-0626-04 N32-3008-41 N87-3008-41	SPRING WASHER BINDING HEAD M SCREW FLAT HEAD MACH BRAZIER HEAD T	IACHINE S	EW					
N	1A, 3A		N87-3008-46	BRAZIER HEAD TA	APTITE SC	CREW			}		
101 101 102	2B 2B 3B 3B		X45-3240-10 X45-3240-11 X45-3260-10 X45-3260-11	FINAL UNIT FINAL UNIT DRIVE UNIT DRIVE UNIT				K K2 K K2			

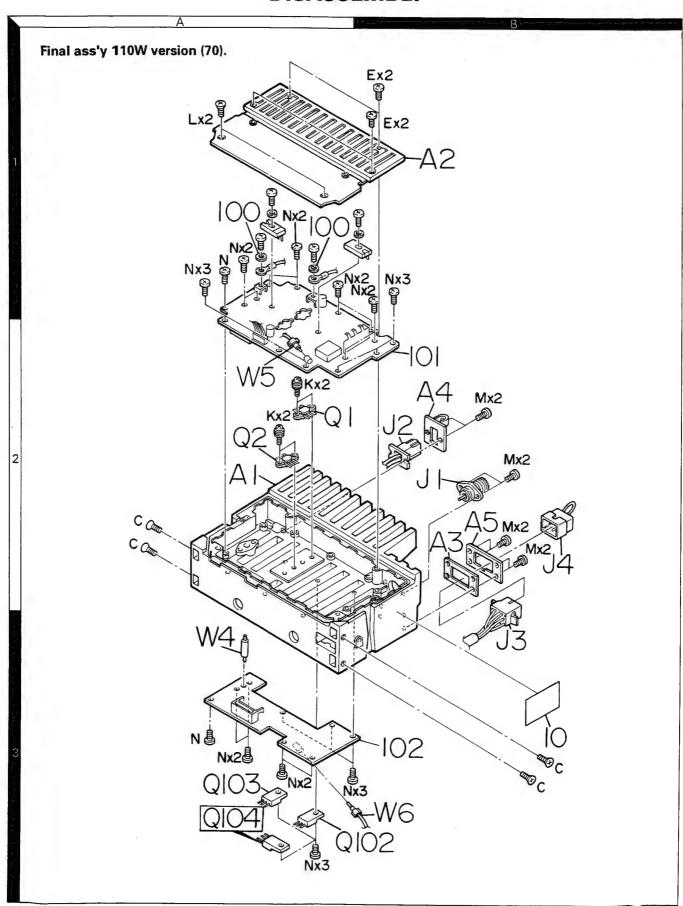
E : Scandinavia & Europe U : PX (Far East, Hawaii)
UE : AAFES (Europe)

K: USA T : England

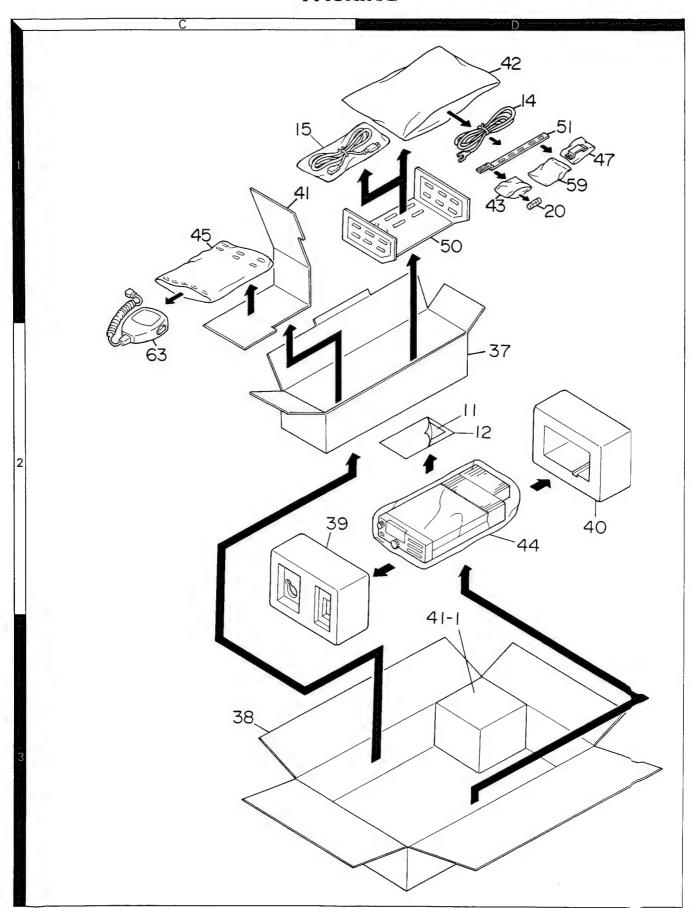
P : Canada M: Other Areas

X : Australia

DISASSEMBLY



PACKING



ADJUSTMENT

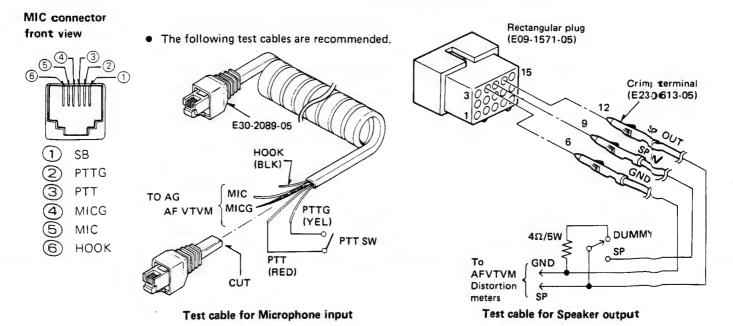
Test equipment required for alignment

	Test Equipment		Major Specifications
1.	Standard Signal Generator	Frequency range	29 to 50MHz
	(SSG)	Modulation	Frequency modulation and external modulation.
	, , ,	Output	$0.1\mu V$ to greater than $1mV$
2.	Power meter	Input impedance	50 ohms
		Operation frequency	29 to 50MHz or more.
		Measurement capability	Vicinity of 200W.
3.	Deviation meter	Frequency range	29 to 50MHz.
4.	Digital Volt Meter	Measuring range	1~15V DC.
		Accuracy	High input impedance for minimum circuit loading.
5.	Oscilloscope		DC through 30MHz.
6.	High sensitivity	Frequency range	10Hz to 100MHz.
	frequency counter	Frequency stability	0.2 ppm or less.
7.	Ammeter		30A.
8.	AF Volt Meter	Frequency range	50Hz to 10kHz.
	(AFVTVM)	Voltage range	3mV to 3V.
9.	Audio Generator (AG)	Frequency range	50Hz to 5kHz or more.
		Output	0 and 1V.
10.	Distortion meter	Capability	3% or less at 1kHz.
		Input level	50mV to 10Vrms.
11.	Voltmeter	Measuring range	1.5 to 10V DC or less.
		Input impedance	50 kohms/V or greater.
12.	4 ohm dummy load		Approx. 4 ohm, 5W.
13.	Regulated power supply		13.4V, approx. 30A.
			(adjustable from 9~16V)
			Useful if ammeter equipped.

The set has been adjusted for the frequencies shown in the following table. When required, re-adjust them following the adjustment procedure to obtain the frequencies you want in actual operation.

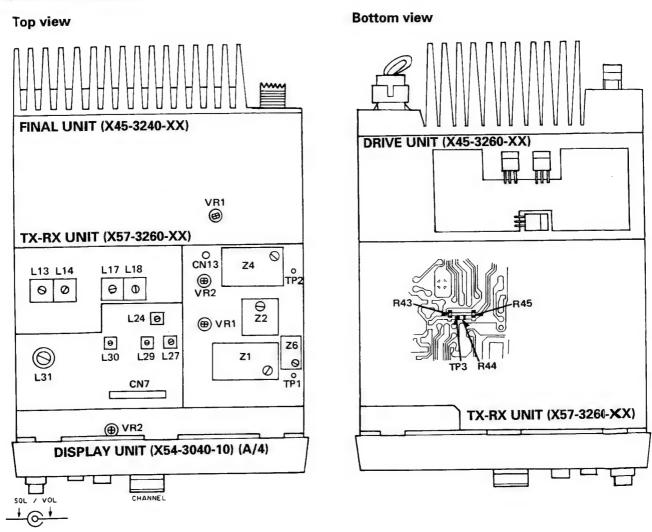
	RX fre	eq'fR () MHz	TX fre	eq'fT () MHz	
		L	M	Н	L	М	Н
TK-620(H)	K	35.9	36.4	36.9	36.0	36.5	37.0
110-020(11)	K2	48.9	49.4	49.9	49.0	49.5	50.0

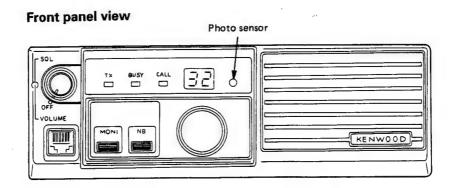
L : Low freq' M : Mid freq' H : Hi freq'



ADJUSTMENT

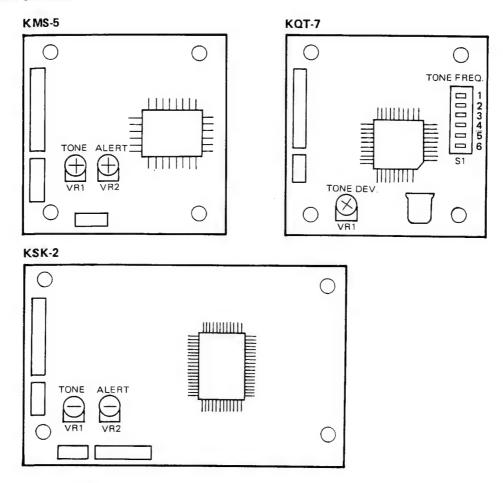
Adjustment location





ADJUSTMENT

Adjustment points



Confirmation of control function (in case KSK-2 control data has not been set.) (Please check only when the KSK-2 (Option) is added.)

ltem	Condition	Operation check		
	No conditions are specified. Microphone (MIC): OFF HOOK SQL VOL: CW MAX MONI SW: OFF			
	Number of channels to be at least two.			
Confirmation of DEL/ADD function	1) SCAN SW : OFF Press DEL/ADD SW	If DEL/ADD lamp has been turned off, it is turned on and "pip" sound is heard once.		
	2) SCAN SW : ON Press DEL/ADD SW	If DEL/ADD lamp has been turned on, it is turned off and "pip" sound is heard twice.		
2. Confirmation of operation of channel switch	1) SCAN SW : OFF 2) SCAN SW : ON	When channel switch is turned, indicated channel changes.		
Check of SCAN and selectable priority	1) SCAN SW : ON MIC : ON HOOK	Channel display indicates		
Channel	2) MIC : OFF HOOK or MONI SW : ON SQL VOL : CCW MAX	Channel before scanning to be indicated.		
4. Confirmation of PTT	1) SCAN SW : OFF PTT SW : ON	TX display to be turned on.		
	2) PTT SW : OFF 3) PTT SW : ON MIC : ON HOOK	TX display to be turned off.		

TK-620(H) TK-620(H)

ADJUSTMENT

		Me	nt	Adjustment				
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
Setting	1) Write freq' designed with EEPROM writer. 2) Connect the power cable to the rear panel. 3) Final unit VR1: MAX CCW. 4) Power SW: ON		F VTVM	IC CONTRACTOR		13.4V	Coupler	Deviation meter Freq' counter
. Common PLL Lock	CH : Channel with highest TX FREQ' (fTH).	Power meter	TX-RX	TP1	TX-RX	Z1	6.0V ADJ.	±0.1V
Voltage 8. TX PLL Lock Voltage	PTT : ON	D∨M		TP2		Z4	5.0V ADJ. Maximize voltage if it does not reach to 5V.	±0.1V
l. TCXO Freq' ADJ.	1) CH: Channel with TX center FREQ' (fTM). PTT: OFF (RX) Note: TCXO is adjusted precisely at 25°C. If it is readjusted, the frequency stability is changed. Do not touch it normally.	Power meter FREQ' counter	TX-RX	TP3 (Foil side)	TX-RX	Z6	f + 10.7MHz ADJ.	±50Hz
i. Power ADJ. (APC)	1) CH : Channel with TX center FREQ' (fTM). PTT : ON	Power meter Ammeter	Rear panel	ANT	Final	TC1 TC2 TC3	ADJ. MAX power (VR1 : MAX CW.)	121W or more.
						VR1	113W ADJ.	±3W, 22A or less.
	2) CH: Channel with lowest TX FREQ' (fTL) and channel with highest TX FREQ' (fTH). PTT: ON						Check	105W to 120W. 22A or less.
5. Protection	1) CH: Channel with lowest TX FREQ' (fTL) and channel with highest TX FREQ' (fTH). ANT: Short and open PTT: ON	Ammeter	Rear	Power connec- tor			Check	20A or less.
7. Transmit Freq' ADJ.	1) CH: Channel with TX center FREQ' (fTM). ANT: Power meter PTT: ON	Power meter FREQ' counter	Rear panel	ANT	TX-RX	Z2	FREQ' ADJ. of TX	±50Hz
	2) CH: Check other channel. PTT: ON						Check	

ADJUSTMENT

		Measurement			Adjustment			
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
8. Tone deviation	CH : Set the channel selector to the channel with which	Power meter	Rear panel	ANT	KMS-5	VR1	±0.75kHz	± 100Hz
ADJ. KMS-5, KQT-7 and	QT (CTCSS) is used. Deviation meter filter: LFP:3kHz, HPF: OFF,	Deviation meter			KQT-7			
KSK-2 (Option)	De-emphasis : OFF PTT : ON							
9. Maximum deviation ADJ.	1) Connect AG to the MIC terminal. AG: 1kHz/50mV Deviation meter filter: LPF: 20kHz, HPF: 50Hz, De-emphasis: 750µsec. TX-RX unit VR1: MAX CW. CH: Channel with highest TX FREQ' (fTH). PTT: ON	Power meter Deviation meter	Rear panel	ANT	TX-RX	VR2	±4.4kHz ADJ. (±4.9kHz ADJ. when signaling unit installed.) Adjust one more than the other by switching between —P and +P.	±100Hz
10. MIC sensitivity ADJ.	1) AG ::1kHz/5mV				TX-RX	VR1	±3.0kHz ADJ. (±3.75kHz ADJ. when signaling unit installed.)	±100Hz
11. Sensitivity ADJ.	1) CH : Channel with RX center FREQ' (fRM).	AF VTVM 4Ω dummy load	Rear panel	EXT.SP	Front panel	AF VOL.	0.78V/4Ω (Noise)	
				TK-6	320(H)	A) A)	CC Disto	ortion meter
	2) SSG FREQ' : fRM Output : 500µV/-53dBm MOD : OFF	SSG AF VTVM Distortion meter 4Ω dummy load	Rear panel	EXT.SP	TX-RX	L13 L14 L17 L18	Reduce noise level using L13, L14, L17 and L18. Decrease the SSG output so that noise level is always 20 to 30dB lower than 0.45V. Repeat 3 to 4 times.	
	3) SSG Output : 500µV/-53dBm MOD : 1kHz/±3kHz DEV.					L31	Adjust for maximum AF output.	
	4) SSG Output : 0.32μV/-117dBm					L24 L27 L29 L30	Adjust for maximum SINAD.	
					Front panel	AF VOL.	0.45V/4Ω ADJ.	lowap 45 iz
	5) CH : Channel with lowest RX FREQ' (fRL) and channel with highest				Front panel	AF VOL.	Check 0.45V/4Ω ADJ.	SINAD 12dB or more.
	RX FREQ' (fRH). SSG FREQ' : fRL or fRH Output : 0.32µV/—117dBm						Check	SINAD 12dB or more.

ADJUSTMENT

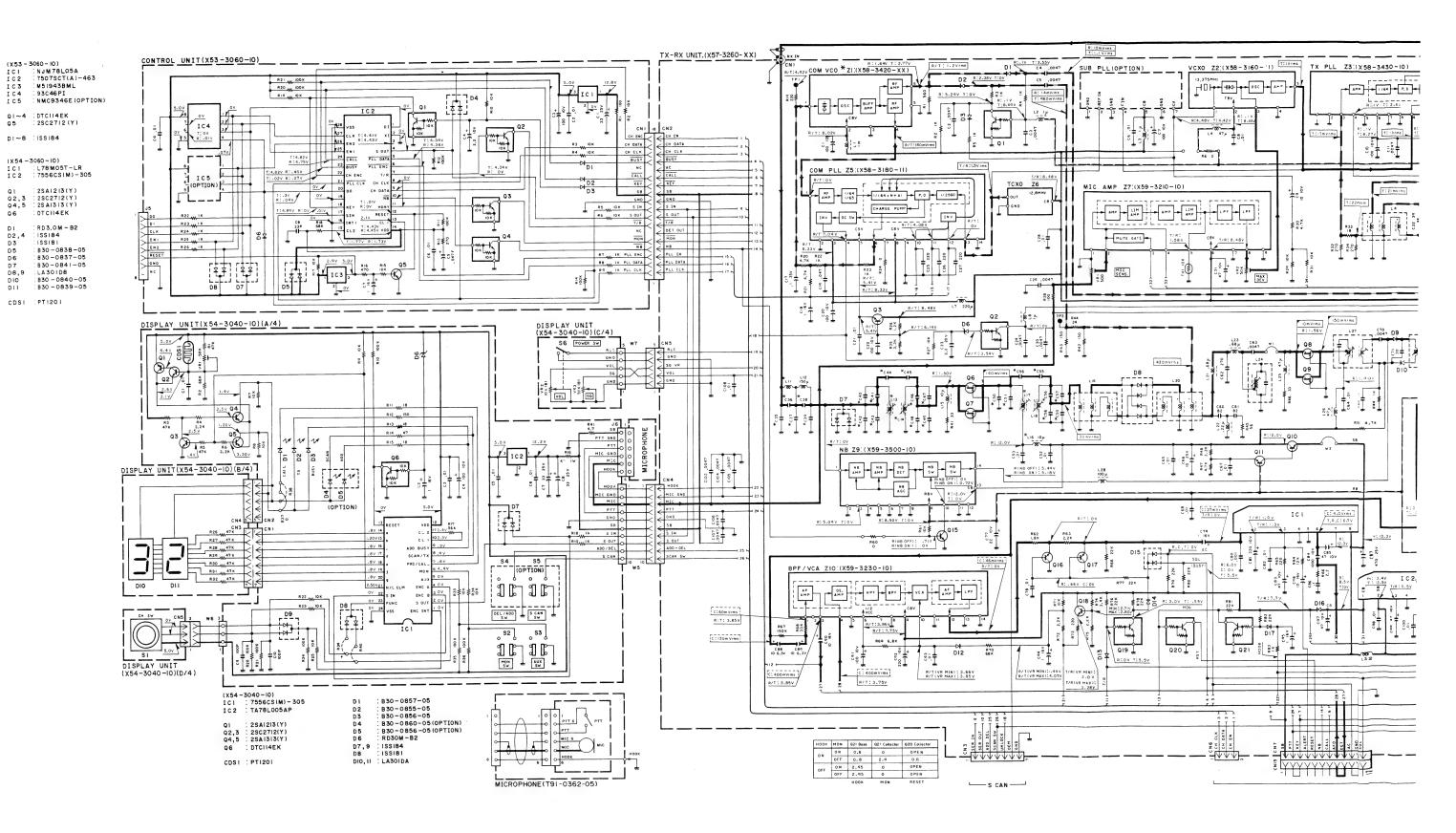
		Measurement		Ac	ljustment			
Item	Condition	Test- equipment	Unit	Terminal	Unit	Parts	Method	Specifications/Remarks
	6) CH: Channel with RX center FREQ' (fRM). SSG FREQ': fRM Output: 500µV/-53dBm	SSG AF VTVM Distortion meter 4Ω dummy	Rear panel	EXT.SP	Font Panel	AF VOL.	4.0V/4Ω ADJ. Check	S/N 50dB or more. Distortion : 5% or less.
12. Squelch	SSG Output : OFF Rotate SQUELCH to a point at which noise disappears. 2) SSG	SSG AF VTVM 4Ω dummy load Oscillo-	Rear panel	EXT.SP				SQL index angle : 8:00 ~ 10:00 Squelch should open.
	Output : 0.2µV/—121dBm	scope						BUSY LED should light.
13. NB	1) SSG Output : 1.58µV/—103dBm MOD : 1kHz/±3kHz DEV.	SSG Oscillo- scope Noise generator	Rear panel	EXT.SP			NB : ON	Confirm noise reduce by NB on. NB LED should light.
14. Adjusting decoder sensitivity for signaling squelch KMS-5, KQT-7 and KSK-2 (Option)	FREQ' of the channel mentioned above. SSG Output: Turn the SSG output so that the SINAD sensitivity becomes 10dB.			AG1 1kH: AG2 AG2 OT to freq'	10 10 10	^ к	EXT. MOD G OUT	
	2) SSG MOD SW: EXT. MOD AG1 FREQ': 1kHz AG2 FREQ': QT tone freq'. 3) AG1: Power switch OFF. AG2 Output: Adjust the output level of AG2 so that SSG deviation becomes 0.75kHz	K 0	(MS-5 (QT-7 r (SK-2) · · · · · · · · · · · · · · · · · · ·	K-620(H	ACC (SP)		AF VTVM Dscilloscope
	4) AG1: Power switch ON. AG1 Output: Adjust the output level of AG1 so that the SSG deviation becomes 3.75kHz. (i.e., QT tone frequency/ 0.75kHz deviation, +1kHz/3kHz deviation) MIC hook: ON hook MONI SW: OFF		Rear panel	EXT.SP	KMS-5 KQT-7 KSK-2		Check	Open.
15. Auto	1) Blind the photo sensor				Display	VR2	Adjust : Center	
dimmer	on the front panel.						position Check	The LED display should be
16. ALERT tone level ADJ.	The ALERT tone is generated wh The ALERT tone level can be adj						out-time operation, et	dimly lit.

TERMINAL FUNCTIONS

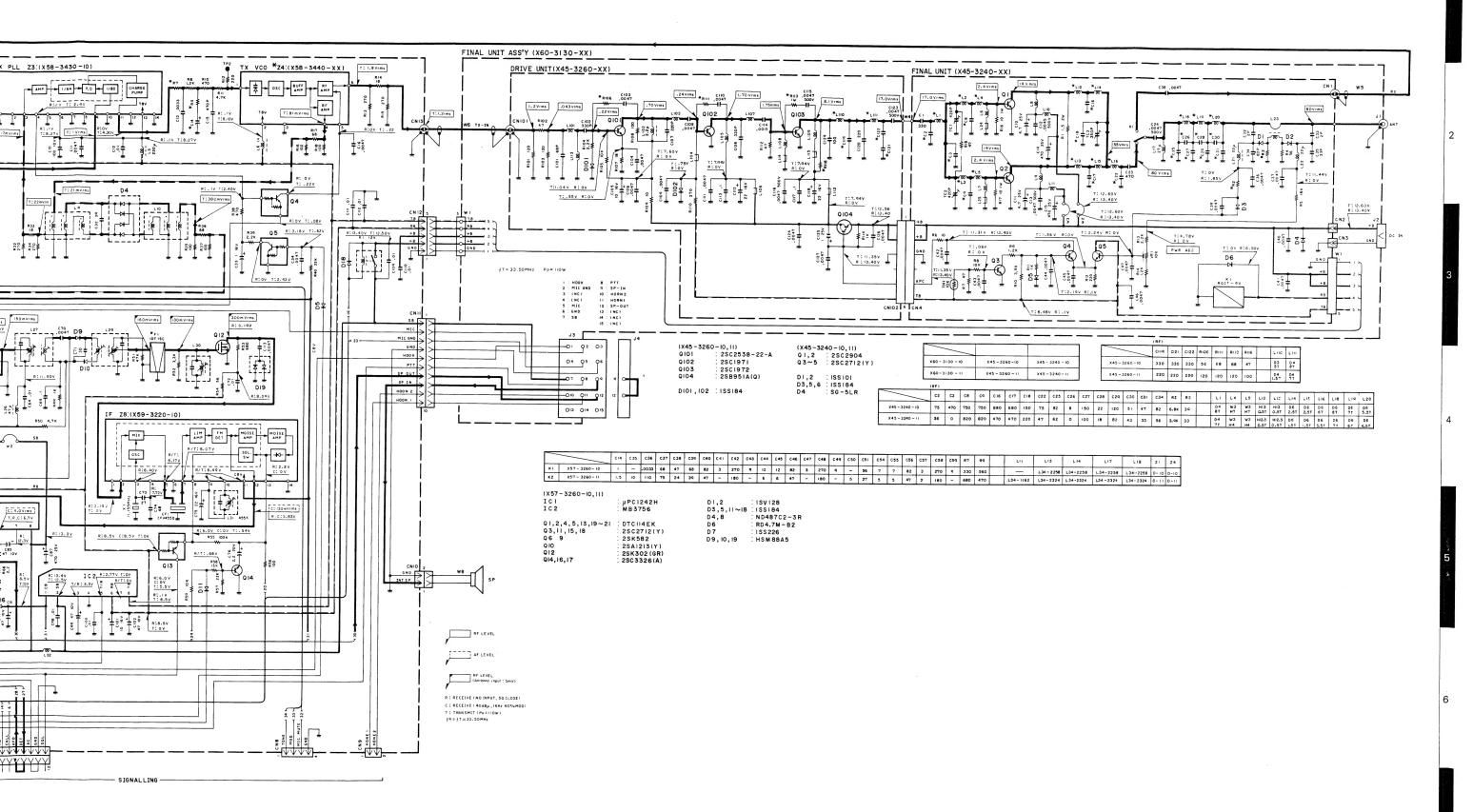
Connector No.	Terminal No.	Terminal Name	Terminal Function				
FINAL ASS'Y (X60-3130-XX)							
CN1	_	RX	Receive signal output,				
			coaxial connector.				
CN2	_	+B	Power supply input.				
CN3	_	GND	GND.				
CN4	1	+B	Power supply output (input).				
(CN102)	2	+B	Power supply output (input).				
,	3	GND	GND.				
	4	_	No connect.				
	5	APC	APC control signal output (input)				
	6	Т8	8V output (input) during TX.				
CN101		TX	Transmission drive input,				
0,,,,,,,			coaxial connector.				
W1	1	GND	GND.				
** 1	2	+B	Power supply output (to TX-RX).				
	3	+B	Power sypply output (to TX-RX)				
	4	R8	8V input during reception.				
	5	T8	8V input during transmission.				
1444							
W4	-	-	Connecting cable between final				
			unit to final unit.				
J1	-	ANT	For connection of antenna,				
			M-type coaxial connector.				
J2	1 1	+B	Power supply input.				
	2	GND	GND.				
J3	1	HOOK	HOOK signal (mic hook) input.				
	2	MIC GND	MIC GND.				
	3	_	No connect.				
	4	_	No connect.				
	5	MIC	MIC signal input.				
	6	GND	GND.				
	7	SB	Power supply output via				
			the power switch.				
	8	PTT	PTT signal input.				
	9	SP IN	Internal speaker input				
			(for control head).				
	10	HORN2	HORN control output.				
	11	HORN1	HORN control output.				
	12	SP OUT	External spaker output.				
	13	_	No connect.				
	14	_	No connect.				
	15	_	No connect.				

06

SCHEMATIC D

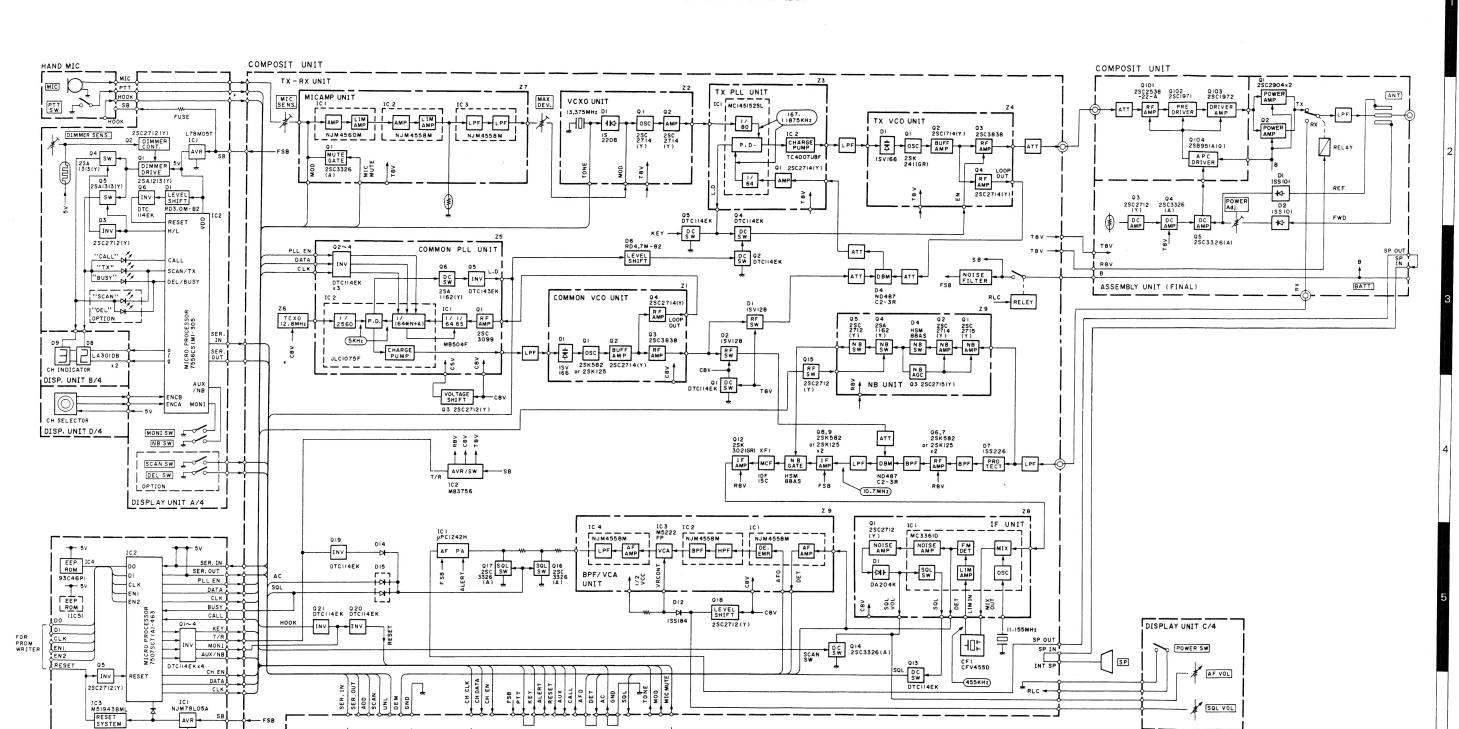


IEMATIC DIAGRAM



TK-620(H) TK-620(H)

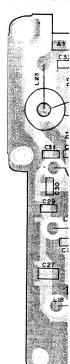




FINAL UI



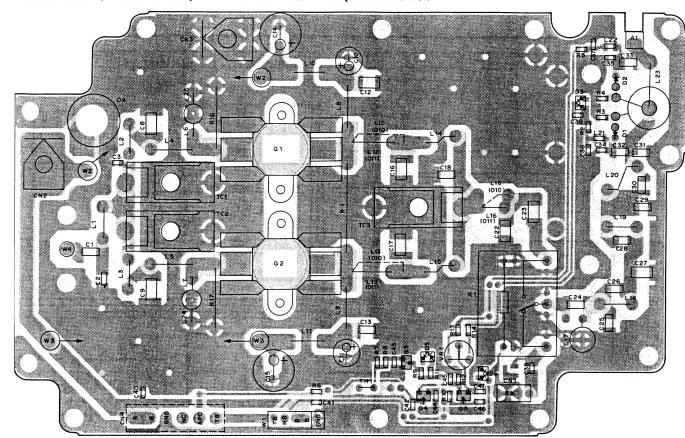
Foil side vi



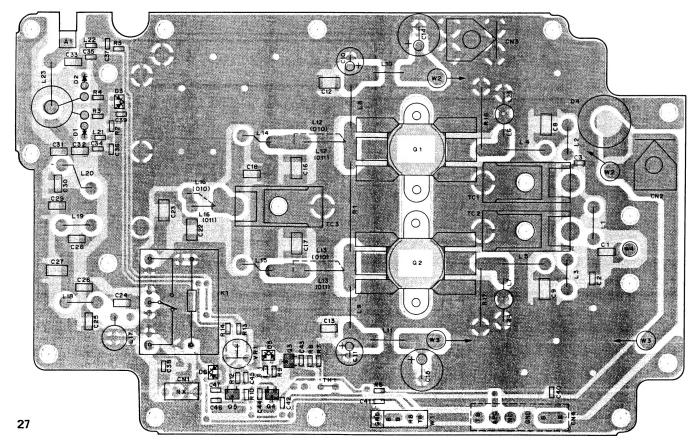
CONTROL UNIT

TK-620(H) PC BOARD VIEWS

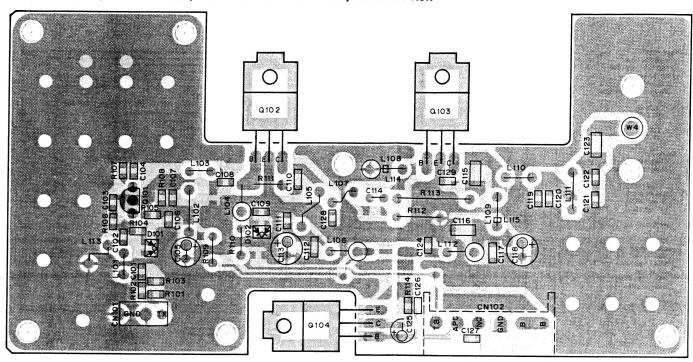
FINAL UNIT (X45-3240-XX) -10 : K -11 : K2 Component side view



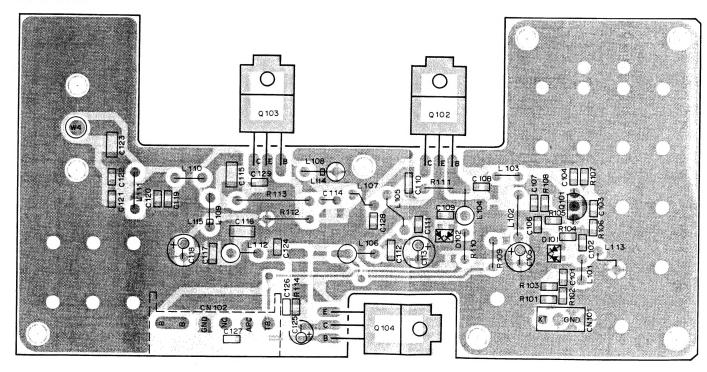
Foil side view



DRIVE UNIT (X45-3260-XX) -10 : K -11 : K2 Component side view



Foil side view



2SC2712

2SB951A

2SC2538-22-A

2SC1971 2SC1972

SPECIFICATIONS

GENERAL

 Frequency Range
 29.7 to 50MHz

 Number of Channels
 32 semi-duplex channels (Modifiable to 64 semi-duplex channels)

 Chennel Spacing
 20kHz (PLL channel step 5kHz)

 Input Voltage
 13.4V DC negative ground

 Current Drain
 0.4A on standby

 1.0A on receive
 25A on transmit

 Duty Cycle
 Receiver 100%, Transmitter 20%

 Temperature Range
 -30°C to +60°C (-22°F to +140°F)

 Dimensions
 7.05" (179mm) W x 2.36" (60mm) H x 12.36" (314mm) D

 Weight
 8.38lbs (3.8kg)

RECEIVER

RF Input Impedance 50Ω Sensitivity

EIA 12dB SINAD $0.25\mu V$ 20dB Quieting $0.35\mu V$ Squelch Sensitivity $0.2\mu V$ threshold

Modulation Acceptance $\pm 7kHz$ Selectivity -80dBIntermodulation -75dBSpurious and Image Rejection -85dBAudio Power Output 4W at 4Ω less than 5% distortion

Audio Power Output 4W at 4Ω less than 5% distortion Frequency Stability $\pm 0.002\%$ from -30°C to +60°C

Channel Frequency Spread 1MHz

(Measurements made per EIA standard EIA-204-C)

TRANSMITTER

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